

A Full Parallel Preconditioner for a Class of M -Matrices

L. BRUGNANO AND D. TRIGIANTE

Dipartimento di Matematica, Università di Bari

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Abstract. A full parallel preconditioner for preconditioned conjugate gradient (PCG) methods is derived, by using an incomplete cyclic reduction, for a class of M -matrices.

1. INTRODUCTION

Let us consider the linear system:

$$Ax = b. \quad (1.1)$$

to be solved with a PCG method. Let A be a five-diagonal, symmetric, weakly diagonally dominant M -matrix, with structure:

$$A = \begin{bmatrix} \diagup & & & & \\ & \diagdown & & & \\ & & \diagup & & \\ & & & \diagdown & \\ & & & & \diagup \end{bmatrix}_{N \times N}. \quad (1.2)$$

The extreme diagonals have distance k from the main one ($N = k^2$). Let us suppose $k = 2^r$.

If P_N is the permutation matrix which takes first the odd rows and then the even ones, one has:

$$P_N \cdot A \cdot P_N^T = \begin{bmatrix} T_1 & S^T \\ S & T_2 \end{bmatrix}, \quad (1.3)$$

with $T_1, T_2, S \in \mathbb{R}^{N/2 \times N/2}$. Moreover, the blocks T_i are symmetric tridiagonal, with band-size $k/2 = 2^{r-1}$, diagonally dominant, while S is upper bidiagonal. Let us consider the block LU factorization of (1.3):

$$P_N \cdot A \cdot P_N^T = \begin{bmatrix} T_1 & 0 \\ S & B_1 \end{bmatrix} \cdot \begin{bmatrix} I & T_1^{-1} \cdot S^T \\ 0 & I \end{bmatrix},$$

with

$$B_1 = T_2 - S \cdot T_1^{-1} \cdot S^T. \quad (1.4)$$

The matrix B_1 is no more a sparse one. To obtain a parallel preconditioner for a PCG method, one can use a sparse approximation of B_1 . For example, we can choose a five-diagonal one, \tilde{B}_1 , with the same structure of A (see (1.2)), but with dimension and band-size halved, by suppressing the other diagonals. It is known that B_1 and \tilde{B}_1 are strongly diagonally dominant M -matrices (see [2]).

If this is the case, we can reduce \tilde{B}_1 by repeating the above operations. After j steps, we choose \tilde{B}_j as the tridiagonal matrix:

$$\tilde{B}_j = \begin{bmatrix} \diagup & & \\ & \diagdown & \\ & & \diagup \end{bmatrix}$$

In the last table the number of iterations and the work/ N is reported, for the values of r considered.

Method	$r = 3$	$r = 4$	$r = 5$	$r = 6$	$r = 7$
CG	10 - 200	22 - 440	44 - 880	90 - 1800	179 - 3580
IC	8 - 192	13 - 312	23 - 552	42 - 1008	71 - 1704
MIC	8 - 192	12 - 288	17 - 408	24 - 576	36 - 864
INV	5 - 180	7 - 252	12 - 432	20 - 720	36 - 1296
MINV	5 - 180	7 - 252	11 - 396	16 - 576	22 - 792
BT0	6 - 246	9 - 369	16 - 656	29 - 1189	55 - 2255
BT1	6 - 246	8 - 328	14 - 574	23 - 943	44 - 1804
MBT0	7 - 287	10 - 410	14 - 574	18 - 738	28 - 1148
MBT1	7 - 287	11 - 451	19 - 779	37 - 1517	96 - 3936

From these results it seems that some of the preconditioners here introduced (in particular MBT0) can be very effective on a parallel computer. For example for $r = 7$ one can use 32 processors obtaining a theoretical speed-up (MBT0 with respect to MINV) given by $S_{32} \cong 22$.

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Dipartimento di Matematica, Universita' di Bari, Italy